

DATE: 9/25/2000

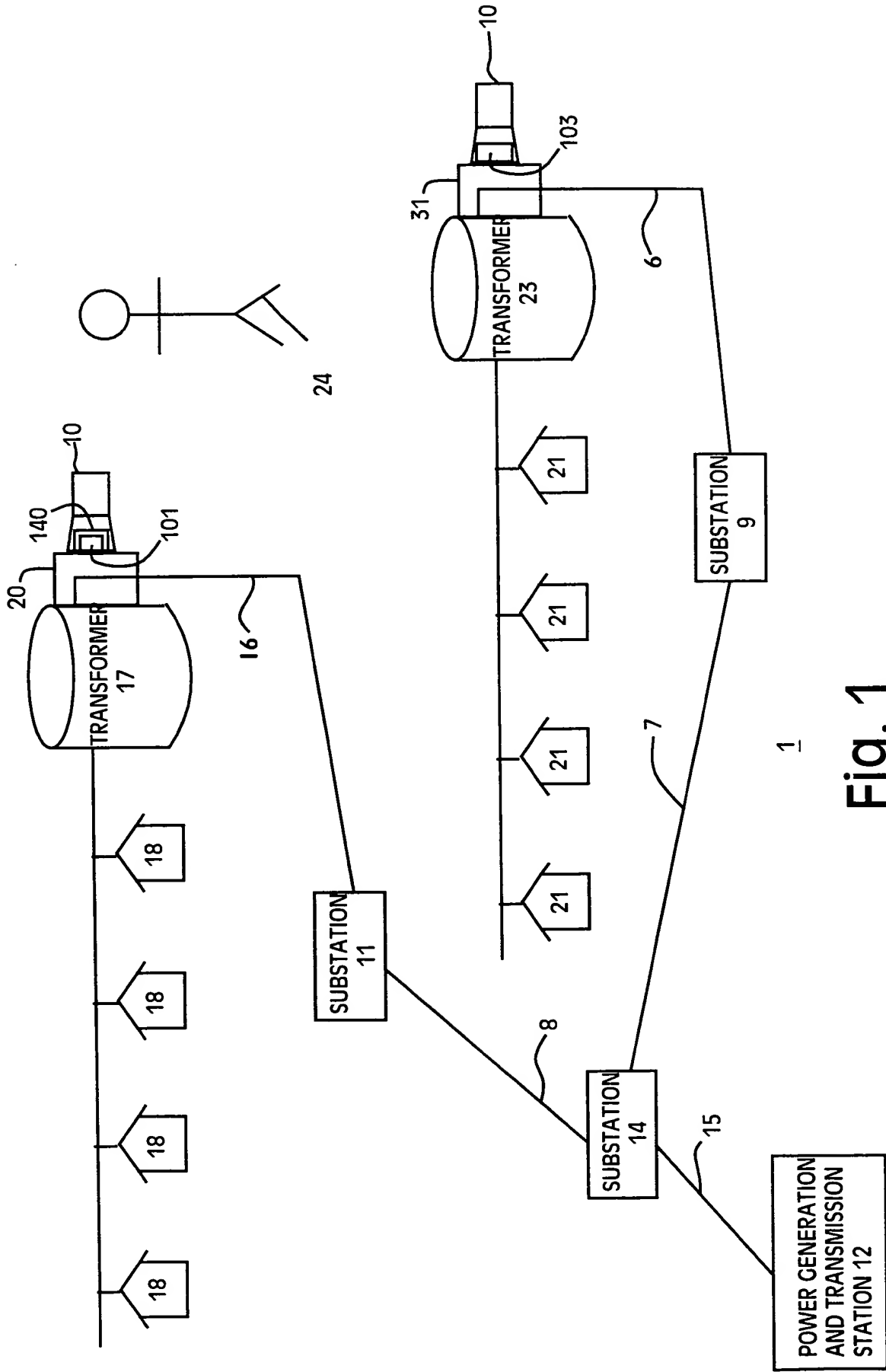


Fig. 1

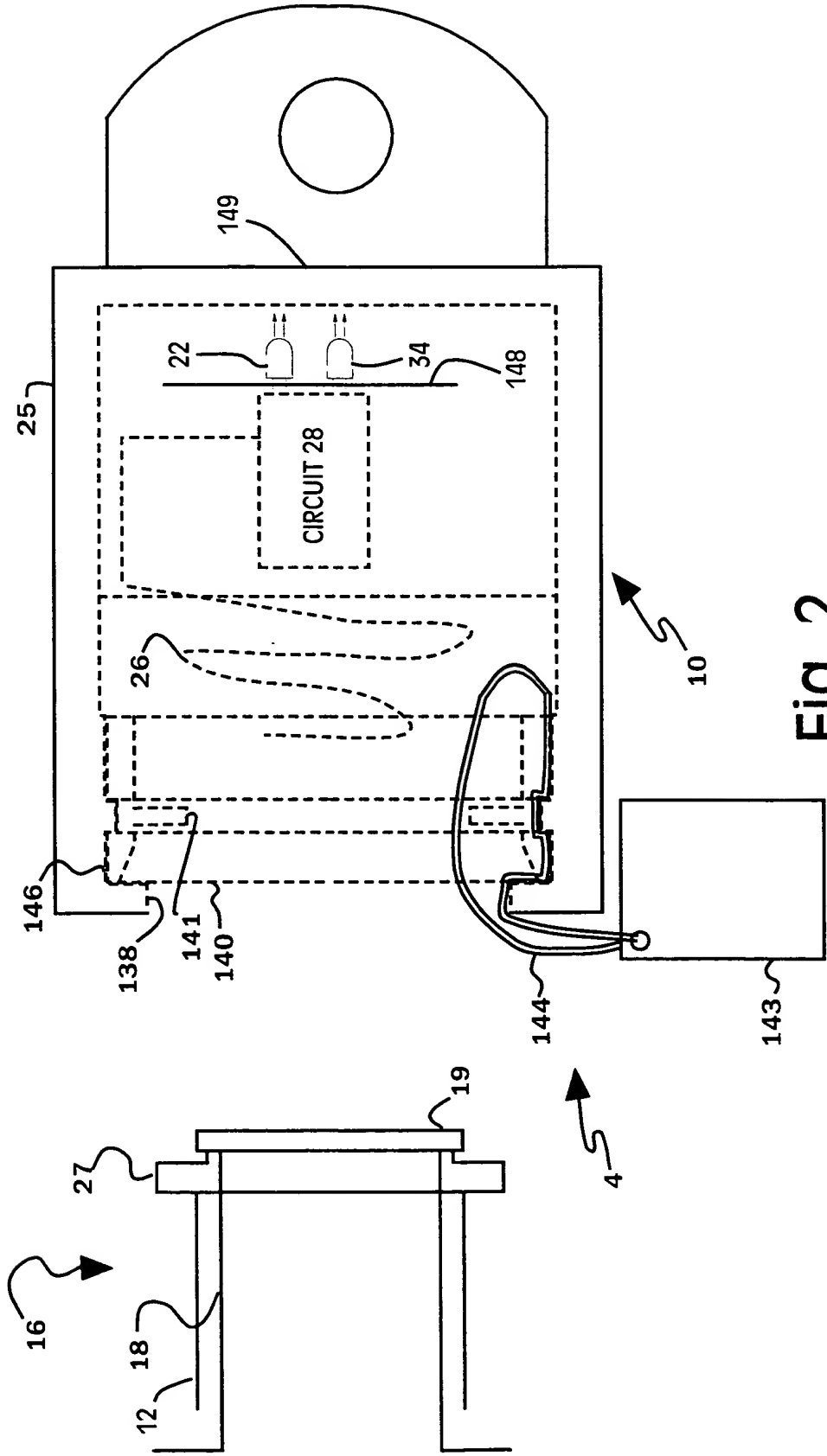


Fig. 2

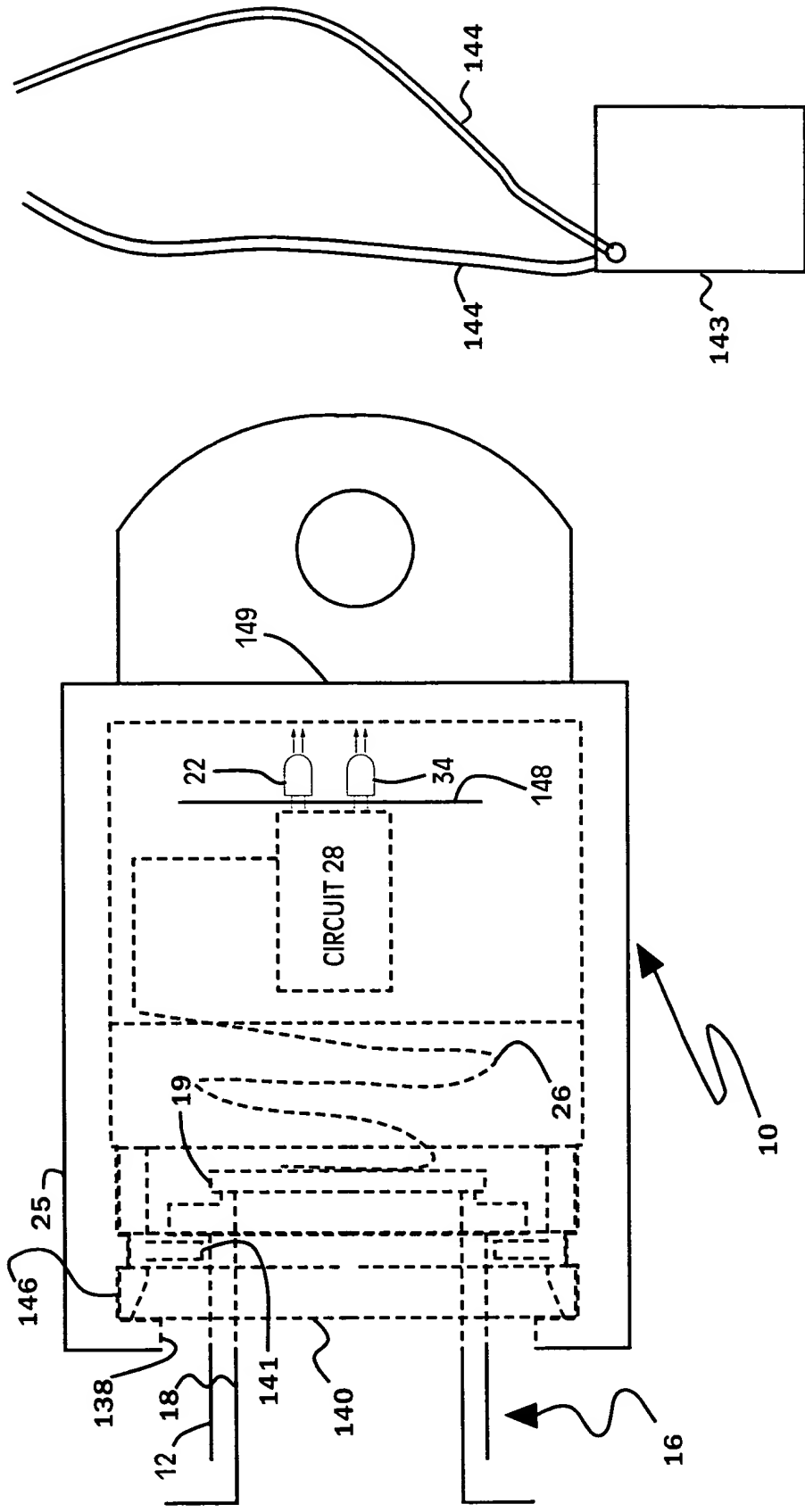


Fig. 3

10.

5

Hand-drawn schematic diagram of a circuit on grid paper. The circuit includes a 3-position DIP switch at the top, connected to a network of resistors (R14, R13, R12, R9, R8, R10, R11, R17, R16) and capacitors (C4, C5). It features a PUT (2N6027) and an SCR (5712) in the center, and a manual reset switch (SW2) on the right. Various component values are specified, such as 40mA, 20mA, 1KΩ, 10Ω, 3.3MΩ, 100Ω, 470pF, 220Ω, 3mA, and 0.014F. Pin numbers (e.g., 85, 86, 67, 68, 75, 70, 73, 78, 80, 92, 94) are also indicated. The diagram is labeled with "58" at the bottom center.

58



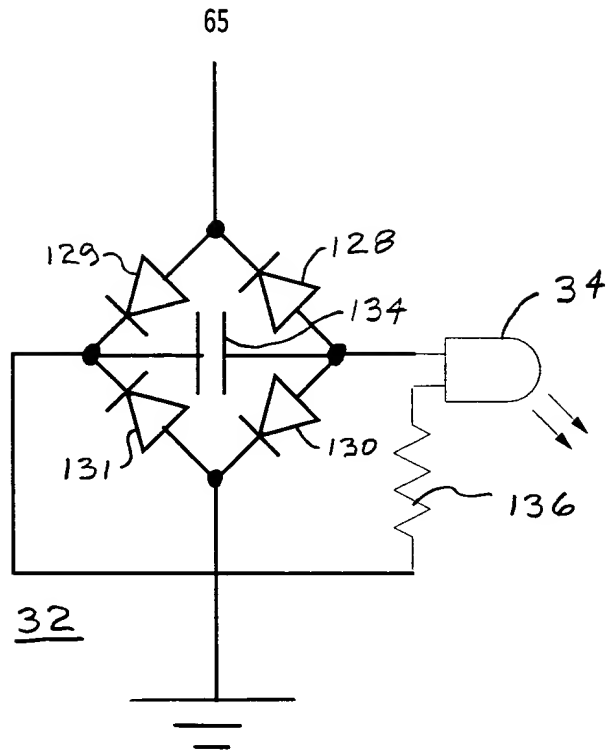


Fig. 8